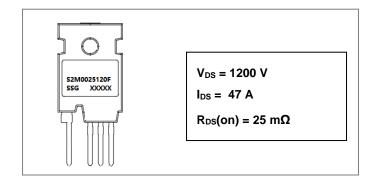
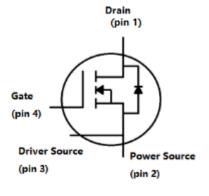




# S2M0025120F 1200V SIC POWER MOSFET



### **Circuit Diagram**



### **Description**

S2M0025120F is a single SiC Power MOSFET packaged in TO-247-4 full case. The device is a high voltage n-channel enhancement mode MOSFET that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S2M0025120F is ideal for energy sensitive, high frequency applications in challenging environments.

#### **Features**

- · Positive temperature characteristics, easy to parallel.
- Low on-resistance Typ. RDS(on) =  $25m\Omega$ .
- Fast switching speed and low switching losses.
- Very fast and robust intrinsic body diode.
- · Process of non-bright Tin electroplating
- "-A" is an AEC-Q101 qualified device

#### **Applications**

- EV Fast Charging Modules
- EV On Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS
- SMPS (Switch Mode Power Supplies)
- DC-DC Converters
- ESS (Energy Storage Systems)

### Maximum Ratings(T=25°C unless otherwise specified)

Characteristics	Symbol	Condition	Max.	Units
Drain Source Voltage	V <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>DS</sub> = 100uA, T <sub>C</sub> = 25°C	1200	V
Gate Source Voltage	V <sub>GSS</sub>	T <sub>C</sub> = 25°C, Absolute maximum values, AC (f>1Hz)	-10 to +25	V
Gate Source Voltage	V <sub>GSOP</sub>	T <sub>C</sub> = 25°C Recommended Operational Values	-5 to +20	V
Continuous Drain Current	ID	Ves = 20V, T <sub>C</sub> = 25°C	47	А
	ID	V <sub>GS</sub> = 20V, T <sub>C</sub> = 100°C	33	А
Pulsed Drain Current	I <sub>D,pulse</sub>	Pulse width t <sub>P</sub> limited by T <sub>jmax</sub>	250	А
Power Dissipation	P <sub>D</sub>	T <sub>C</sub> =25°C, T <sub>J</sub> = 175 °C	190	W

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### Electrical Characteristics(T=25°C unless otherwise specified)

Characteristics	Symbol	Condition	Min.	Тур.	Max.	Units	
Drain Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 100uA	1200			V	
		$V_{DS} = V_{GS}$ , $I_D = 15mA$	1.8	2.3	4	V	
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 15mA, T <sub>J</sub> = 175 °C		1.4		V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 1200V, V <sub>GS</sub> = 0V			100	uA	
Gate Source Leakage Current	Igss	V <sub>GS</sub> = 20V, V <sub>DS</sub> = 0V			250	nA	
Drain Source On-State		V <sub>GS</sub> = 20V, I <sub>D</sub> = 50A		25	34	mΩ	
Resistance	$R_{DS(on)}$	V <sub>GS</sub> = 20V, I <sub>D</sub> = 50A, T <sub>J</sub> = 175 °C		32		mΩ	
		V <sub>DS</sub> = 20 V, I <sub>DS</sub> = 50 A		21		S	
Transconductance	gfs	V <sub>DS</sub> = 20 V, I <sub>DS</sub> = 50 A, T <sub>J</sub> = 175 °C		23		S	
Input Capacitance	Ciss	Ves = 0V,		4054			
Output Capacitance	Coss	V <sub>DS</sub> = 1000V		246		pF	
Reverse Transfer Capacitance	Crss	V <sub>AC</sub> = 25mV		17			
Coss Stored Energy	Eoss	f = 1MHz		129		uJ	
Turn-On Switching Energy	Eon	V <sub>DS</sub> = 800V, V <sub>GS</sub> = -5/20V		1.5			
Turn-Off Switching Energy	Eoff	$I_D = 50A, R_{G(ext)} = 2.5\Omega$		0.3		mJ	
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 800V, V_{GS} = -5/20V$		48			
Rise Time	t <sub>r</sub>	$I_D = 50A$ , $R_{G(ext)} = 2.5\Omega$		18			
Turn-Off Delay Time	t <sub>d(off)</sub>			55		ns	
Fall Time	t <sub>f</sub>			19			
Internal Gate Resistance	R <sub>G(int)</sub>	f = 1MHz, VAC = 25 mV		2.2		Ω	
Gate to Source Charge	Qgs	V <sub>DS</sub> = 800V, V <sub>GS</sub> = -5/20V		33			
Gate to Drain Charge	Q <sub>gd</sub>	I <sub>D</sub> = 50A		67		nC	
Total Gate Charge	Qg			165			

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### **Reverse Diode Characteristics:**

Characteristics	Symbol	Condition	Тур.	Max.	Units
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = -5V, I <sub>SD</sub> = 25A	3.5		V
		V <sub>GS</sub> = -5V, I <sub>SD</sub> = 25A, T <sub>J</sub> = 175℃	3.1		V
Continuous Diode Forward Current	Is	V <sub>GS</sub> = -5V, T <sub>C</sub> = 25°C		130	А
Reverse Recovery Time	t <sub>rr</sub>	V <sub>GS</sub> =-5V, I <sub>SD</sub> =50A, T <sub>J</sub> =25°C	33		ns
Reverse Recovery Charge	Qrr	VR=800V	384		nC
Peak Reverse Recovery Current	I <sub>mm</sub>	dif/dt=1790A/µs	22		Α

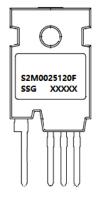
### **Thermal-Mechanical Specifications:**

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	TJ	-	-55 to +175	°C
Storage Temperature	T <sub>stg</sub>	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	R <sub>0</sub> JC	DC operation	0.78	°C/W

### **Ordering Information:**

Device	Package	Shipping
S2M0025120F	TO-247-4 full	30pcs/tube

### **Marking Diagram**



Where XXXXX is YYWWL

 $\begin{array}{ll} S2M & = Device Type \\ 0025 & = R_{DS}(on) \end{array}$ 

120 = Reverse Voltage (1200V)

F = Package SSG = SSG YY = Year WW = Week L = Lot Number

Cautions: Molding resin

Epoxy resin UL:94V-0

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### **Ratings and Characteristics Curves**

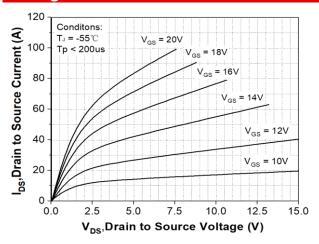


Figure 1. Output Characteristics T<sub>J</sub> = -55 °C

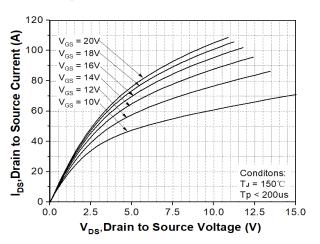


Figure 3. Output Characteristics T<sub>J</sub> = 150°C

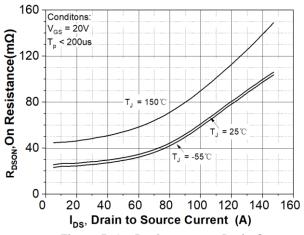


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

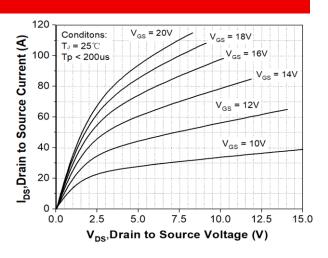


Figure 2. Output Characteristics  $T_J = 25$  °C

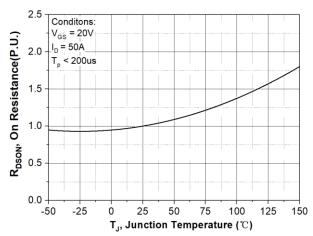


Figure 4. Normalized On-Resistance vs. Temperature

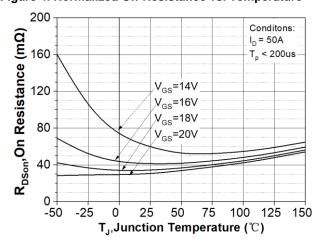


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

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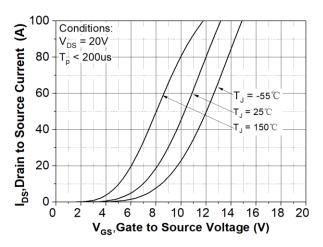


Figure 7. Transfer Characteristic for Various Junction Temperatures

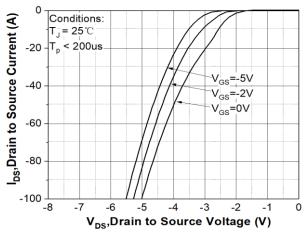


Figure 9. Body Diode Characteristic at T<sub>J</sub> = 25 °C

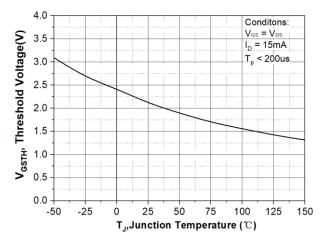


Figure 11. Threshold Voltage vs. Temperature

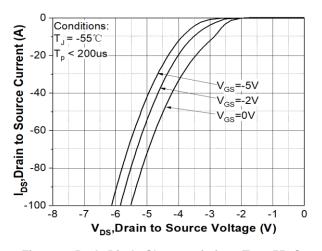


Figure 8. Body Diode Characteristic at  $T_J$  = -55 °C

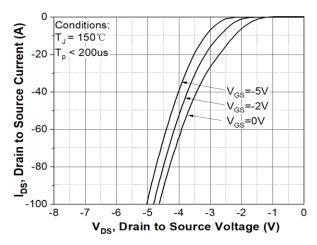


Figure 10. Body Diode Characteristic at T<sub>J</sub> = 150 °C

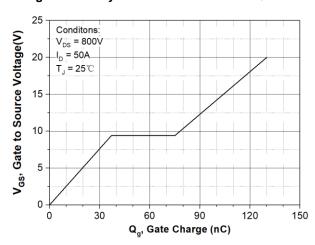


Figure 12. Gate Charge Characteristic

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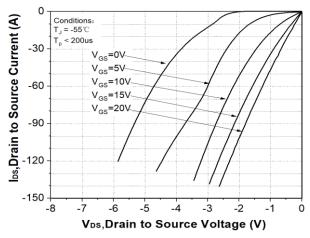


Figure 13. 3rd Quadrant Characteristic at T<sub>J</sub> = -55 °C

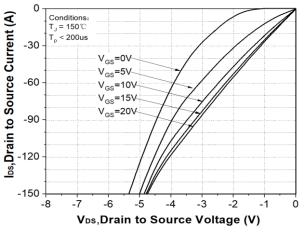


Figure 15. 3rd Quadrant Characteristic at T<sub>J</sub> = 150°C

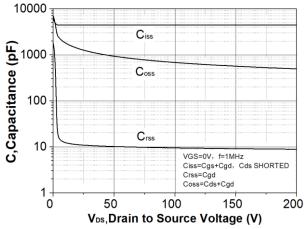
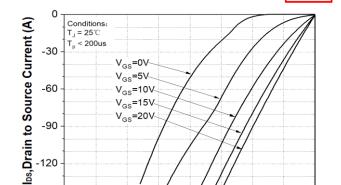


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)



-5

-150

-8

Figure 14. 3rd Quadrant Characteristic at T<sub>J</sub> = 25 °C

-3

-4

VDS, Drain to Source Voltage (V)

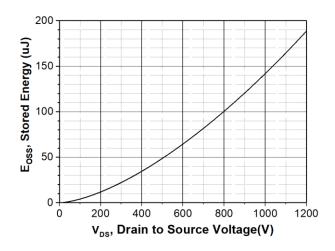


Figure 16. Output Capacitor Stored Energy

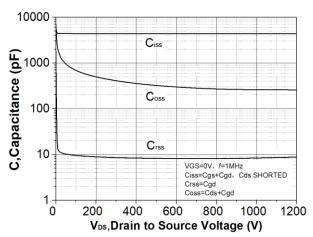


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1000V)

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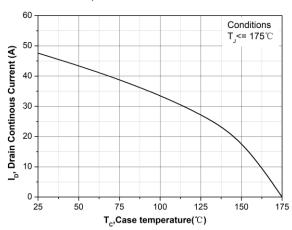


Figure 19. Continuous Drain Current Derating vs.

Case Temperature

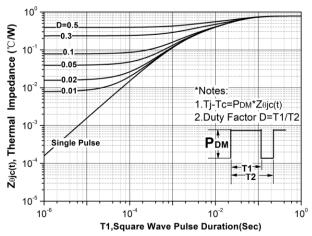


Figure 21. Transient Thermal Impedance (Junction - Case)

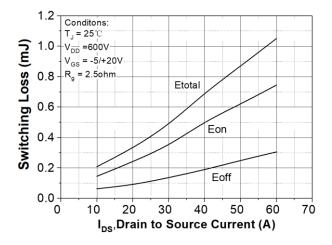


Figure 23. Clamped Inductive Switching Energy vs. Drain Current (V<sub>DD</sub> = 600V)



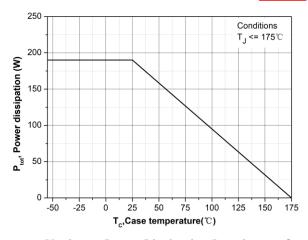


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

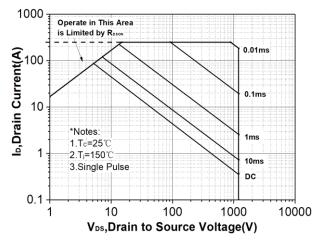


Figure 22. Safe Operating Area

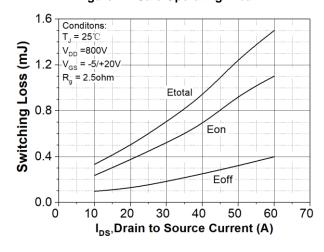


Figure 24. Clamped Inductive Switching Energy vs. Drain Current ( $V_{DD} = 800V$ )

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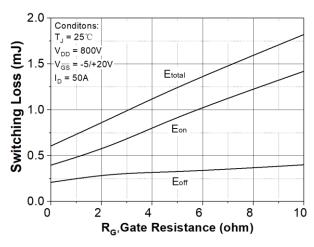


Figure 25. Clamped Inductive Switching Energy vs. R<sub>G(ext)</sub>

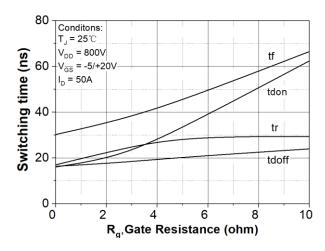


Figure 27. Switching Times vs.  $R_{G(ext)}$ 

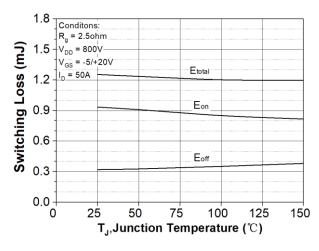


Figure 26. Clamped Inductive Switching Energy vs.
Temperature

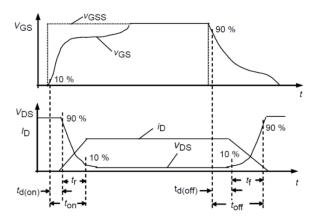
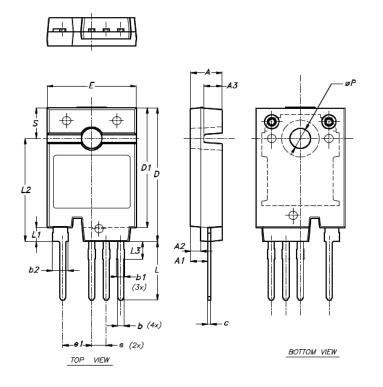


Figure 28. Switching Times Definition





## **Mechanical Dimensions TO-247-4 full**



Dimensions(mm)					
REF. DIM.	NOM	MIN	MAX		
Α		5.50	5.80		
A1		2.85	3.25		
A2	1.92				
A3	3.18				
b		0.95	1.30		
b1		1.10	1.50		
b2		2.50	2.90		
С		0.40	0.80		
D		23.8	24.15		
D1	21.5				
E		15.	15.75		
е	2.54				
e1	5.08				
L		10.2	10.80		
L1		2.20	2.80		
L2	18.5				
L3	3				
ØР		3.55	3.65		
S	5.5				

#### S2M0025120F



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